500.39826CX1/E5828-04EU

JAN 1 3 2005 ES

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Seiichiro KANNO et al.

Serial No.

10/679,342

Filed

7 October 2003

For

SEMICONDUCTOR MANUFACTURING APPARATUS AND

METHOD OF PROCESSING SEMICONDUCTOR WAFER

USING PLASMA, AND WAFER VOLTAGE PROBE

Group

1763

Examiner

Unknown

Conf. No

2653

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.97(b)

Commissioner for Patents POB 1450 Alexandria, Virginia 22313-1450

13 January 2005

Sir:

In the matter of the above-identified application, Applicant hereby submits the attached information under 37 CFR §§1.97 and 1.98 for entry and consideration by the Office, and for printing on the first page of any patent issuing hereon, as listed on the accompanying Form PTO-1449 Equivalent.

In accordance with the Final Rule under 37 CFR §1.98 effective 21 October 2004, no copies of US patents/published applications are submitted herewith.

Additionally, no separate concise explanation of relevance is submitted for each item of information not in the English language In accordance with 37 CFR §1.98(3) because an English language Abstract for each of the references also is attached.

This IDS is timely filed under 37 CFR §1.97(b) prior to mailing of a first Action on the merits in the present case. Applicant submits that all requirements under 37 CFR §§1.97 and 1.98 are complete, and respectfully requests the Examiner to return an initialed copy of the attached Form PTO-1449 Equivalent to indicate entry and consideration of the listed information.

Respectfully submitted,

Paul J. Skwierawski

Registration No. 32,173

ANTONELLI, TERRY, STOUT & KRAUS, LLP 1300 North Seventeenth Street, Suite 1800

Arlington, Virginia 22209-3873, USA

Telephone 703-312-6600 Facsimile 703-312-6666

Attachments:

PTO-1449

3 JP References/English Abstracts

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